



## UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
 United States Patent and Trademark Office  
 Address: COMMISSIONER FOR PATENTS  
 P.O. Box 1450  
 Alexandria, Virginia 22313-1450  
 www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 3302

SERIAL NUMBER 10/623,082	FILING DATE 07/17/2003  RULE	CLASS 257	GROUP ART UNIT 2815	ATTORNEY DOCKET NO. 02-0279/1D / LSI1P199D1
-----------------------------	---------------------------------------	--------------	------------------------	--

## APPLICANTS

Peter A. Burke, Portland, OR;

William K. Barth, Gresham, OR;

## \*\* CONTINUING DATA \*\*\*\*\*

This application is a DIV of 10/272,767 10/16/2002 PAT 6,642,597 ✓ *none*

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

*none*

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 10/20/2003

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY OR	SHEETS DRAWING 5	TOTAL CLAIMS 7/3	INDEPENDENT CLAIMS 1
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged	Examiner's Signature <i>[Signature]</i>	Initials <i>[Initials]</i>		

## ADDRESS

24319  
 LSI LOGIC CORPORATION  
 1621 BARBER LANE  
 MS: D-106  
 MILPITAS, CA  
 95035

## TITLE

Inter-layer interconnection structure for large electrical connections

FILING FEE  RECEIVED	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time )